

Electronic Acknowledgement Receipt

EFS ID:	1642821
Application Number:	10678055
International Application Number:	
Confirmation Number:	4569
Title of Invention:	Method and apparatus for reducing stress across capacitors used in integrated circuits
First Named Inventor/Applicant Name:	Ramkarthik Ganesan
Customer Number:	8791
Filer:	William W. Schaal/Susan McFarlane
Filer Authorized By:	William W. Schaal
Attorney Docket Number:	42P6963R
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		P6963.PDF	449693	yes	12

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment After Final	1	1
	Claims	2	10
	Applicant Arguments/Remarks Made in an Amendment	11	11
	Authorization for Extension of Time all replies	12	12

Warnings:

Information:

2	Oath or Declaration filed	P6963dec.pdf	126154	no	2
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Warnings:

Information:

Total Files Size (in bytes):			575847
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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